Intel Processors Ranked

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Brand** | [**Desktop**](https://en.wikipedia.org/wiki/Desktop_computer) | | | | [**Mobile**](https://en.wikipedia.org/wiki/Laptop) | | | |
| **Code-named** | **Cores** | [**Fab**](https://en.wikipedia.org/wiki/Semiconductor_device_fabrication) | **Date released** | **Code-named** | **Cores** | **Fab** | **Date released** |
| **Core Solo** | Desktop version not available | | | | [Yonah](https://en.wikipedia.org/wiki/Yonah_(microprocessor)) | 1 | 65 nm | January 2006 |
| **Core Duo** | Desktop version not available | | | | [Yonah](https://en.wikipedia.org/wiki/Yonah_(microprocessor)) | 2 | 65 nm | January 2006 |
| **Core 2 Solo** | Desktop version not available | | | | [Merom-L](https://en.wikipedia.org/wiki/Merom_(microprocessor)#Merom-L) [Penryn-L](https://en.wikipedia.org/wiki/Penryn_(microprocessor)#Penryn) | 1 1 | 65 nm 45 nm | September 2007 May 2008 |
| **Core 2 Duo** | [Conroe](https://en.wikipedia.org/wiki/Conroe_(microprocessor)#Conroe) [Allendale](https://en.wikipedia.org/wiki/Conroe_(microprocessor)#Allendale) [Wolfdale](https://en.wikipedia.org/wiki/Wolfdale_(microprocessor)) | 2 2 2 | 65 nm 65 nm 45 nm | August 2006 January 2007 January 2008 | [Merom](https://en.wikipedia.org/wiki/Merom_(microprocessor)) [Penryn](https://en.wikipedia.org/wiki/Penryn_(microprocessor)) | 2 2 | 65 nm 45 nm | July 2006 January 2008 |
| **Core 2 Quad** | [Kentsfield](https://en.wikipedia.org/wiki/Kentsfield_(microprocessor)) [Yorkfield](https://en.wikipedia.org/wiki/Yorkfield_(microprocessor)) | 4 4 | 65 nm 45 nm | January 2007 March 2008 | [Penryn](https://en.wikipedia.org/wiki/Penryn_(microprocessor)) | 4 | 45 nm | August 2008 |
| **Core 2 Extreme** | [Conroe XE](https://en.wikipedia.org/wiki/Conroe_(microprocessor)#Conroe_XE) [Kentsfield XE](https://en.wikipedia.org/wiki/Kentsfield_(microprocessor)#Kentsfield_XE) [Yorkfield XE](https://en.wikipedia.org/wiki/Yorkfield_(microprocessor)#Yorkfield_XE) | 2 4 4 | 65 nm 65 nm 45 nm | July 2006 November 2006 November 2007 | [Merom XE](https://en.wikipedia.org/wiki/Merom_(microprocessor)#Merom_XE) [Penryn XE](https://en.wikipedia.org/wiki/Penryn_(microprocessor)#Penryn_XE) [Penryn XE](https://en.wikipedia.org/wiki/Penryn_(microprocessor)#Penryn_QC) | 2 2 4 | 65 nm 45 nm 45 nm | July 2007 January 2008 August 2008 |
| **Core M** | Desktop version not available | | | | [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) | 2 | 14 nm | September 2014[[9]](https://en.wikipedia.org/wiki/Intel_Core#cite_note-9) |
| **Core m3** | Desktop version not available | | | | [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) | 2 2 2 | 14 nm 14 nm 14 nm | August 2015 September 2016 April 2017 |
| **Core m5** | Desktop version not available | | | | [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) | 2 | 14 nm | August 2015 |
| **Core m7** | Desktop version not available | | | | [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) | 2 | 14 nm | August 2015 |
| **Core i3** | [Clarkdale](https://en.wikipedia.org/wiki/Clarkdale_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake) | 2 2 2 2 2 2 4 4 | 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm | January 2010 February 2011 September 2012 September 2013 September 2015 January 2017 October 2017 Jan. & April 2019 | [Arrandale](https://en.wikipedia.org/wiki/Arrandale_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) [Cannon Lake](https://en.wikipedia.org/wiki/Cannon_Lake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) [Whiskey Lake](https://en.wikipedia.org/wiki/Whiskey_Lake_(microarchitecture)) [Ice Lake](https://en.wikipedia.org/wiki/Ice_Lake_(microarchitecture)) | 2 2 2 2 2 2 2 2 2 2 2 4 2 2 | 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm 14 nm 14 nm 10 nm 14 nm 14 nm 10 nm | January 2010 February 2011 June 2012 June 2013 January 2015 Sept. 2015 & June 2016 August 2016 November 2016 Jan. & June 2017 April 2018 May 2018 July 2018 August 2018 May & Aug. 2019 |
| **Core i5** | [Lynnfield](https://en.wikipedia.org/wiki/Lynnfield_(microprocessor)) [Clarkdale](https://en.wikipedia.org/wiki/Clarkdale_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake) | 4 2 4 2 2-4 2-4 4 4 4 6 6 | 45 nm 32 nm 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm 14 nm | September 2009 January 2010 January 2011 February 2011 April 2012 June 2013 June 2015 September 2015 January 2017 October 2017 Oct. 2018 & Jan. 2019 | [Arrandale](https://en.wikipedia.org/wiki/Arrandale_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Kaby Lake-R](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) [Whiskey Lake](https://en.wikipedia.org/wiki/Whiskey_Lake_(microarchitecture)) [Ice Lake](https://en.wikipedia.org/wiki/Ice_Lake_(microarchitecture)) | 2 2 2 2 2 2-4 2 4 4 4 4 4 | 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm 14 nm 14 nm 14 nm 10 nm | January 2010 February 2011 May 2012 June 2013 January 2015 September 2015 August 2016 January 2017 October 2017 April 2018 Aug. 2018 & April 2019 May & Aug. 2019 |
| **Core i7** | [Bloomfield](https://en.wikipedia.org/wiki/Bloomfield_(microprocessor)) [Lynnfield](https://en.wikipedia.org/wiki/Lynnfield_(microprocessor)) [Gulftown](https://en.wikipedia.org/wiki/Gulftown_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Sandy Bridge-E](https://en.wikipedia.org/wiki/Sandy_Bridge) [Sandy Bridge-E](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Ivy Bridge-E](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) | 4 4 6 4 6 4 4 4 4-6 4 4 4 6 8 | 45 nm 45 nm 32 nm 32 nm 32 nm 32 nm 22 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm 14 nm | November 2008 September 2009 July 2010 January 2011 November 2011 February 2012 April 2012 June 2013 September 2013 June 2015 August 2015 January 2017 October 2017 October 2018 | [Clarksfield](https://en.wikipedia.org/wiki/Clarksfield_(microprocessor)) [Arrandale](https://en.wikipedia.org/wiki/Arrandale_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Broadwell](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Kaby Lake](https://en.wikipedia.org/wiki/Kaby_Lake) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) [Whiskey Lake](https://en.wikipedia.org/wiki/Whiskey_Lake_(microarchitecture)) [Ice Lake](https://en.wikipedia.org/wiki/Ice_Lake_(microarchitecture)) | 4 2 4 2 2-4 2-4 2 4 2-4 2 4 4-6 4 4 | 45 nm 32 nm 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm 14 nm 14 nm 14 nm 14 nm 10 nm | September 2009 January 2010 January 2011 February 2011 May 2012 June 2013 January 2015 June 2015 September 2015 August 2016 January 2017 April 2018 Aug. 2018 & April 2019 May & Aug. 2019 |
| **Core i7 Extreme** | [Bloomfield](https://en.wikipedia.org/wiki/Bloomfield_(microprocessor)) [Gulftown](https://en.wikipedia.org/wiki/Gulftown_(microprocessor)) [Sandy Bridge-E](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge-E](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell-E](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) [Broadwell-E](https://en.wikipedia.org/wiki/Broadwell_(microarchitecture)) [Skylake-X](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Kaby Lake-X](https://en.wikipedia.org/wiki/Kaby_Lake) | 4 6 6 6 8 10 6-8 4 | 45 nm 32 nm 32 nm 22 nm 22 nm 14 nm 14 nm 14 nm | November 2008 March 2010 November 2011 September 2013 August 2014 May 2016 June 2017 June 2017 | [Clarksfield](https://en.wikipedia.org/wiki/Clarksfield_(microprocessor)) [Sandy Bridge](https://en.wikipedia.org/wiki/Sandy_Bridge) [Ivy Bridge](https://en.wikipedia.org/wiki/Ivy_Bridge_(microprocessor)) [Haswell](https://en.wikipedia.org/wiki/Haswell_(microarchitecture)) | 4 4 4 4 | 45 nm 32 nm 22 nm 22 nm | September 2009 January 2011 May 2012 June 2013 |
| **Core i9** | [Skylake-X](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Skylake-X](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Skylake-X](https://en.wikipedia.org/wiki/Skylake_(microarchitecture)) [Coffee Lake](https://en.wikipedia.org/wiki/Coffee_Lake) | 10 12 14-18 8 | 14 nm 14 nm 14 nm 14 nm | June 2017 August 2017 September 2017 October 2018 | [Coffee Lake-H](https://en.wikipedia.org/wiki/Coffee_Lake_(microarchitecture)) | 6 | 14 nm | April 2018 |

# Why did Intel name their processors as i3, i5 and i7

The number of cores in a processor are usually even numbers, I've never heard of any with odd numbers. Therefore, if the i3 was named i2, people would think it was dual-core; same with if the i5 was named the i4, people would think it was quad-core. Certainly, the i7 would be great as a 6-core processor, but it isn't. The naming with odd numbers avoids confusion in this manner, although I'm sure there are other reasons as well.